

FIG. 1

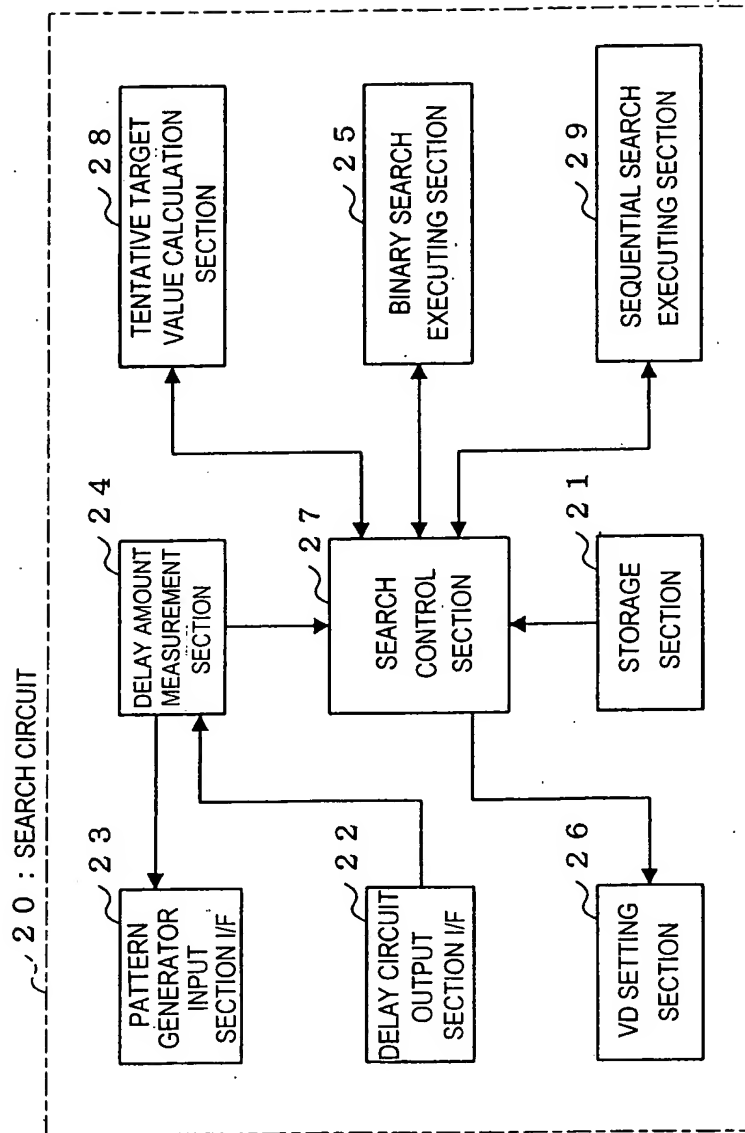


FIG. 2

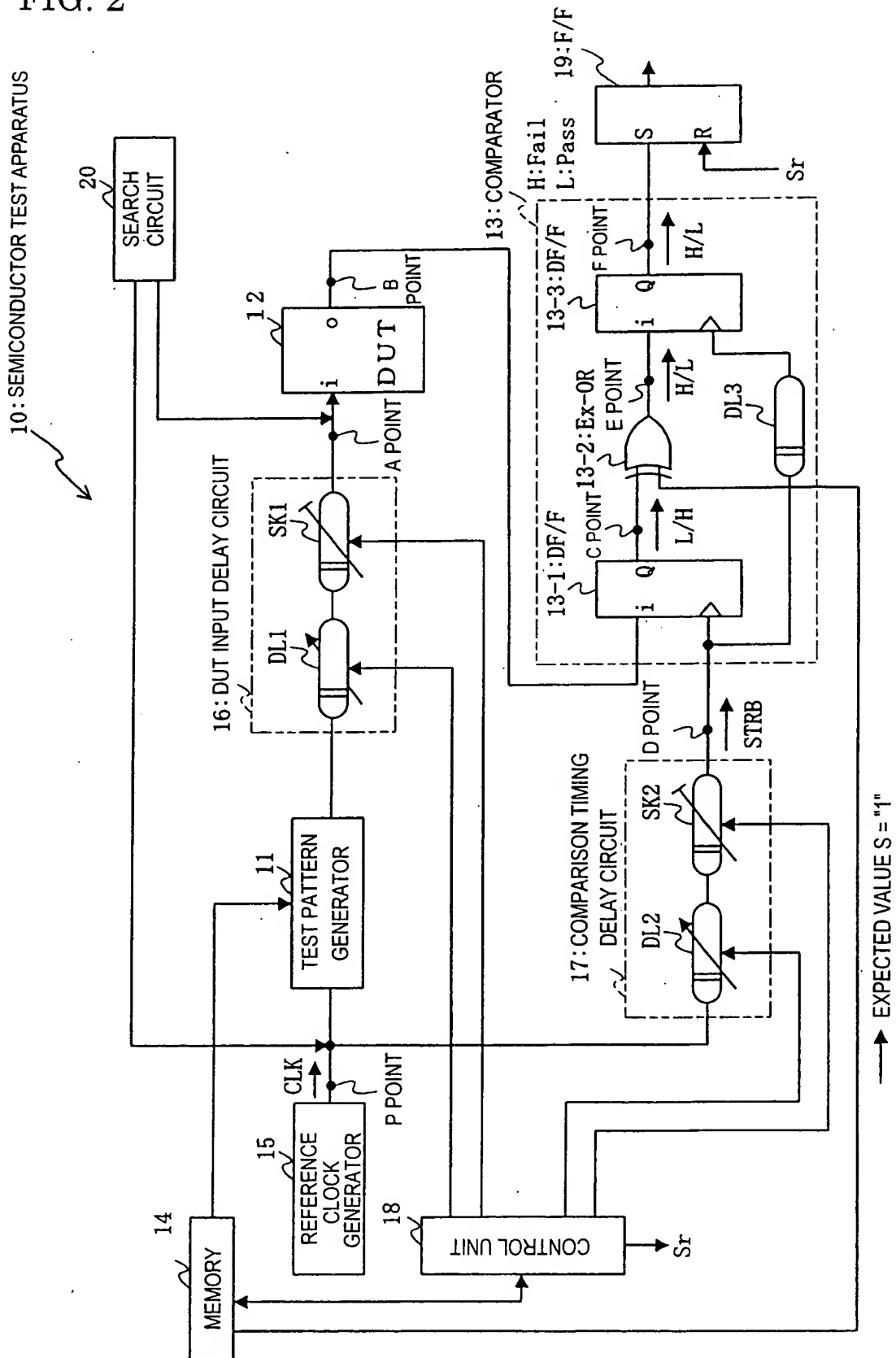
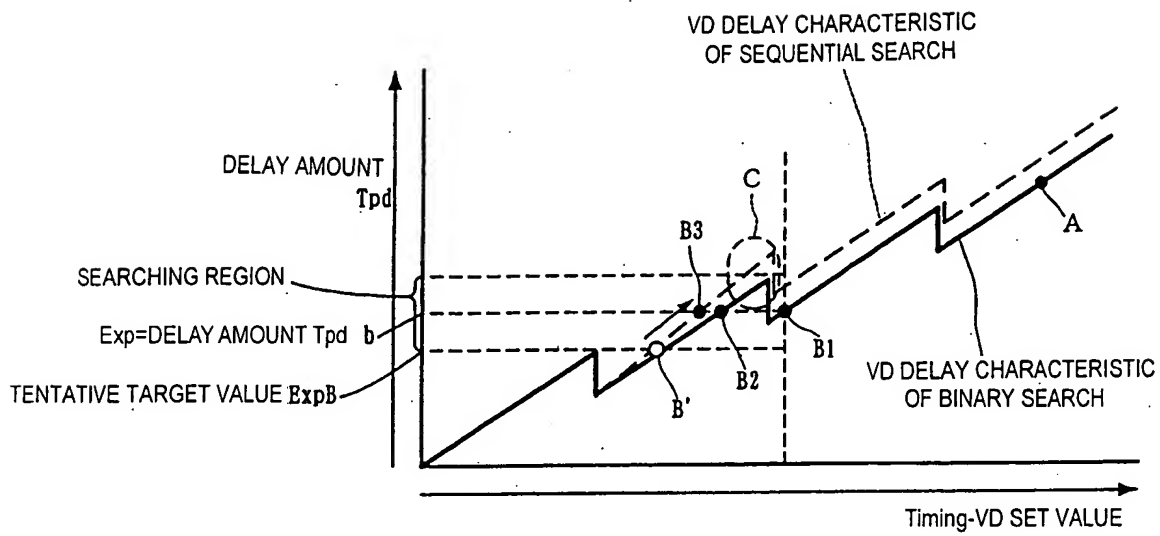


FIG. 3



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FIG. 4

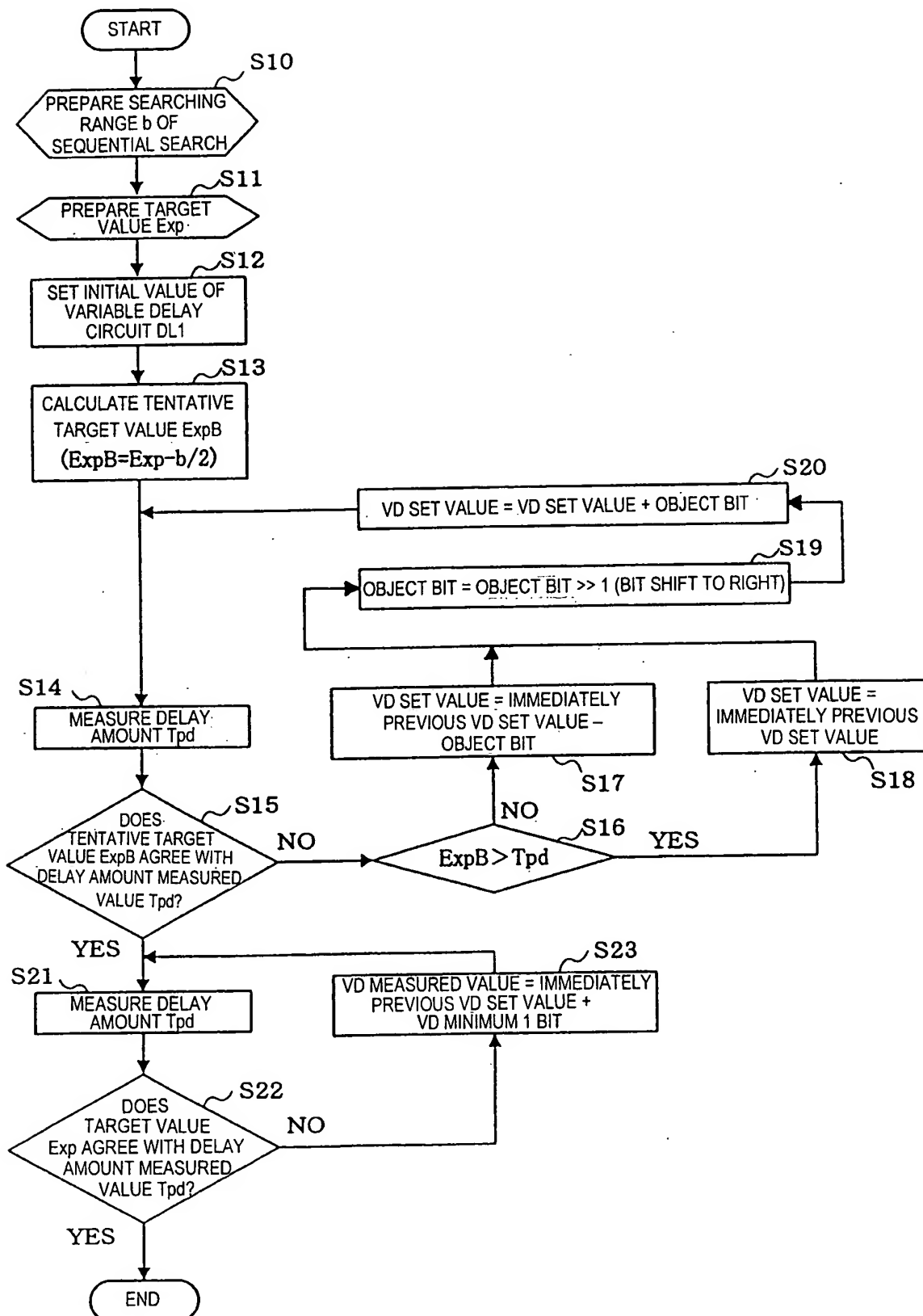
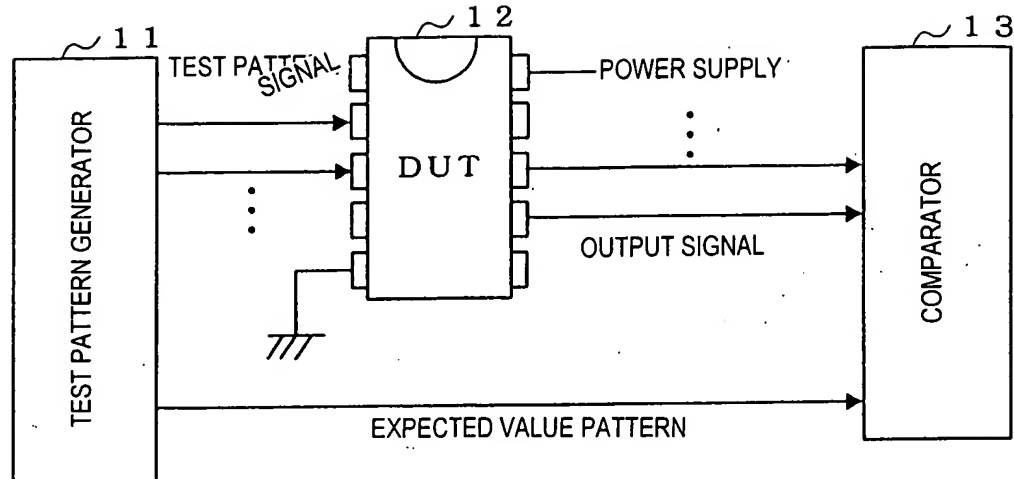
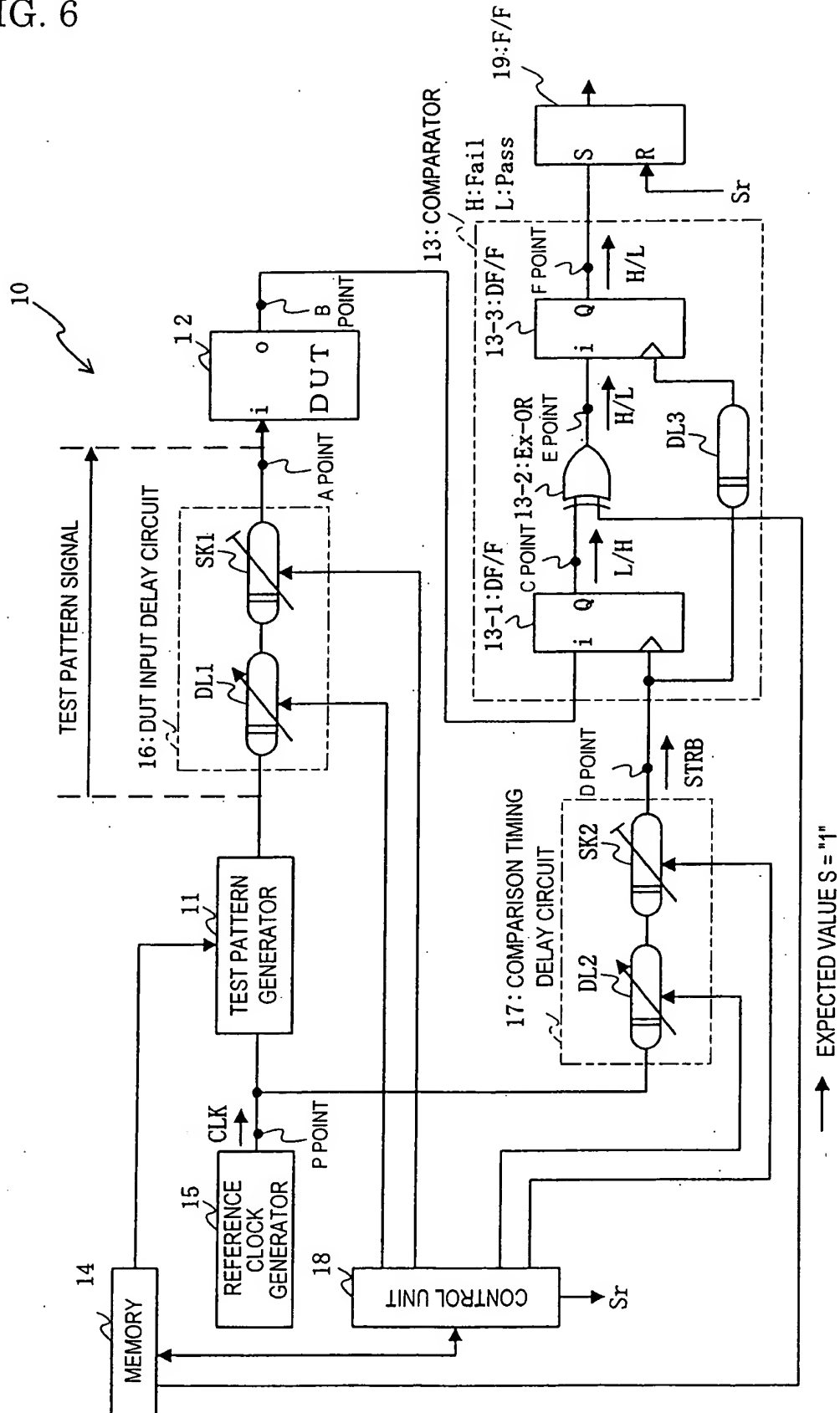


FIG. 5



10 : SEMICONDUCTOR TEST APPARATUS

FIG. 6



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FIG. 7

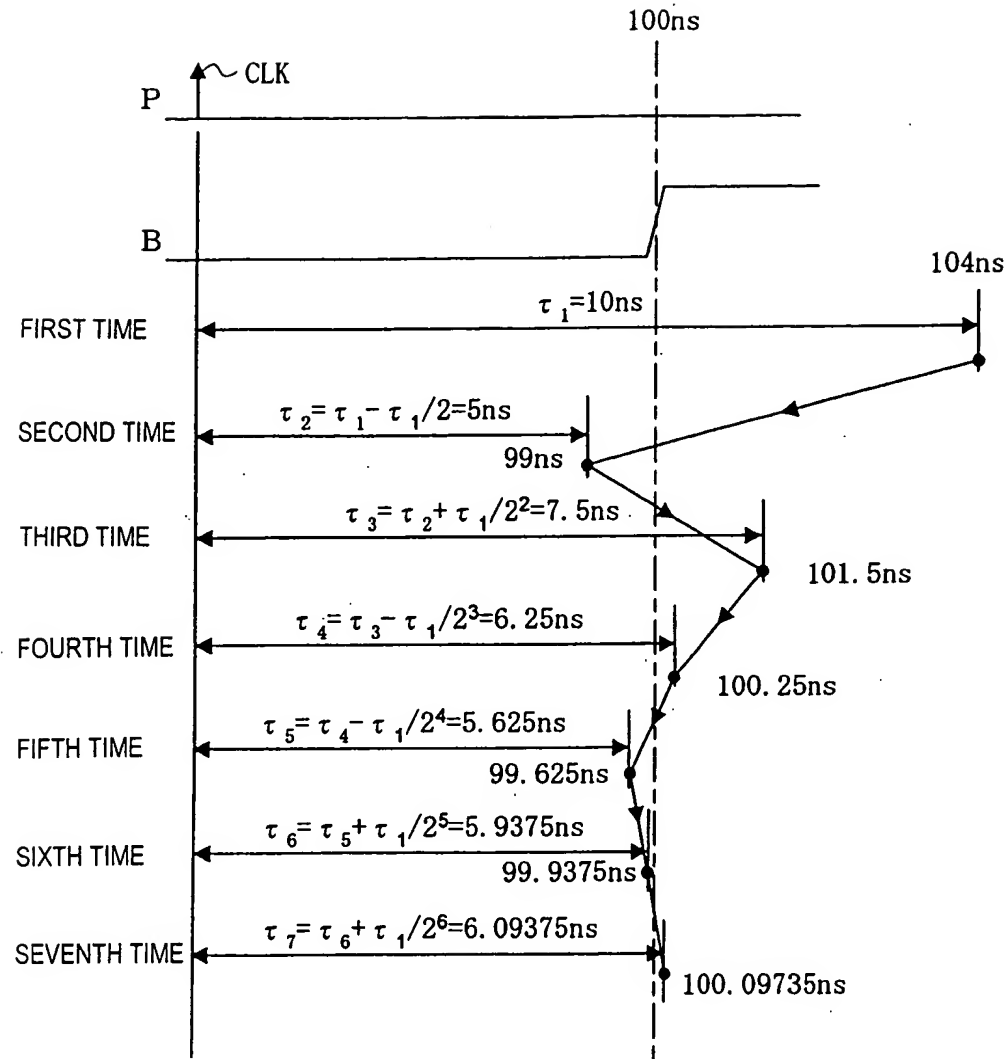


FIG. 8

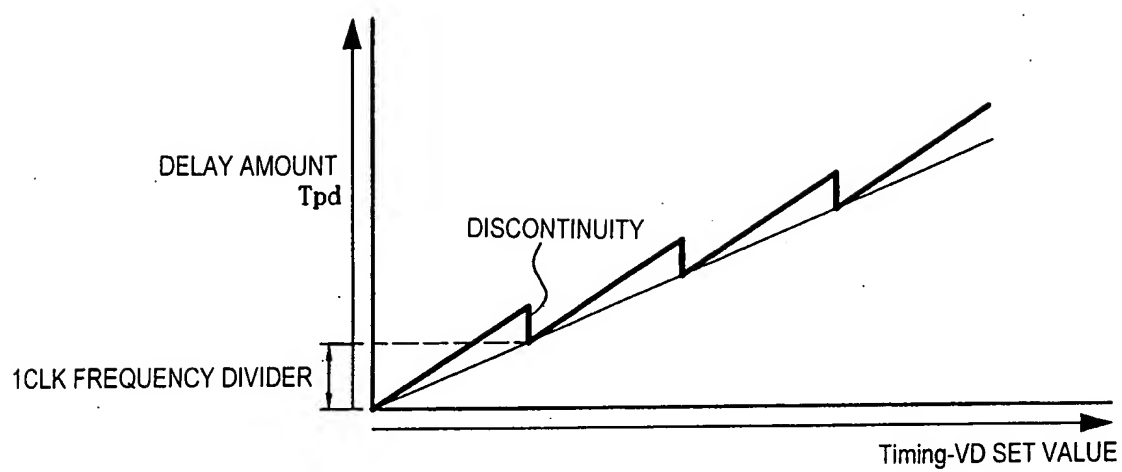
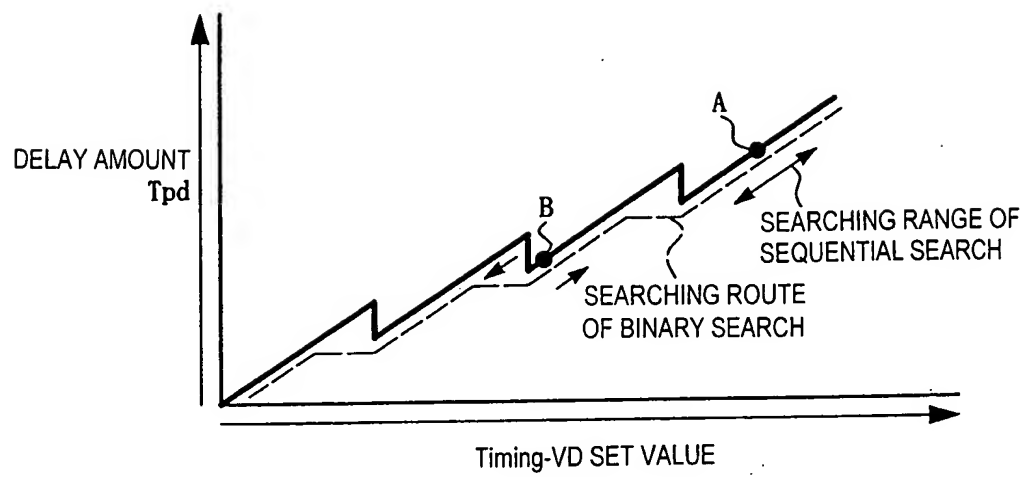




FIG. 9



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FIG. 10

